

Fig.2

32 REGISTER

RETRY ENABLE BIT
ERROR CORRECTION ENABLE BIT
MAXIMUM REQUIRED TIME OF DATA WRITING (WITH RETRY PROCESS, WITHOUT RETRY PROCESS)
RETRY OCCURRENCE FREQUENCY
ERROR OCCURRENCE FREQUENCY
ERROR RATE (WITH ERROR CORRECTION, WITHOUT ERROR CORRECTION)
:

Fig.3

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BLOCK ADDRESS		USAGE STATE (1:UNUSED, 0:USED)
PHYSICAL ADDRESS	LOGICAL ADDRESS	
0	—	1
1	3	0
2	4	0
3	5	0
4	—	1
5	1	0
6	—	0
7	—	1
8	6	0
9	—	1
10	—	1
11	—	1
:	:	:

Fig.4

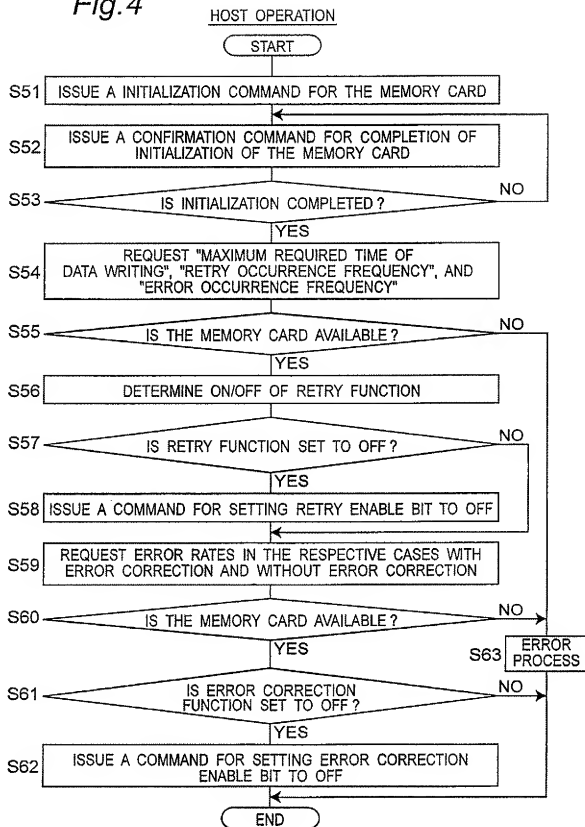


Fig.5

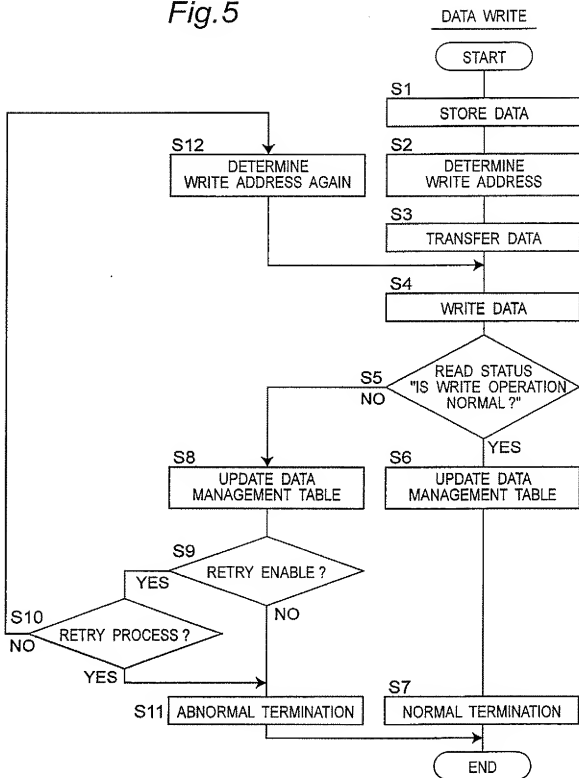


Fig.6

